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Form PTO-1419 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 0325,00446		SERIAL NO. 09/775,372		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICANT: Jiann-Cheng Chen et al.				
		FILING DATE February 1, 2000		GROUP 2151		
U.S. PATENT DOCUMENTS						
Examiner Initial	Document No.	Date	Name	Class	Sub- Class	Filing Date
SKS	3 9 3 6 6 0 4	2/3/1976	Pommerening	178	69.5R	
SKS	4 2 3 1 1 0 4	10/28/1980	St. Clair	364	900	
SKS	4 4 0 0 8 1 7	8/23/1983	Sumner	375	119	
SKS	4 5 6 4 9 5 3	1/14/1986	Werking	377	52	
SKS	4 8 7 3 7 0 3	10/10/1989	Crandall et al.	375	118	
SKS	5 1 3 8 6 3 7	8/11/1992	Fox	375	118	
SKS	5 2 5 6 9 1 2	10/26/1993	Rios	307	269	
SKS	5 3 4 7 5 5 9	9/13/1994	Hawkins et al.	377	54	
SKS	5 8 1 1 9 9 5	9/22/1998	Roy et al.	327	99	
SKS	5 8 5 7 0 0 5	1/5/1999	Buckenmaier	375	357	
SKS	5 8 9 4 5 6 7	4/13/1999	Dodd et al.	395	551	
FOREIGN PATENT DOCUMENTS						
Document No.	Date	Country	Class	Sub- Class	Transl.	
					Yes	No
OTHER DOCUMENTS (Incl. Author, Title, Date, Pertinent Pages, etc.)						
S. Babar Raza et al., "ARCHITECTURE FOR IMPLEMENTING VIRTUAL MULTIQUEUE FIFO", Serial No. 09/676,704, Filed September 29, 2000.						
S. Babar Raza et al., "METHOD AND LOGIC FOR STORING AND EXTRACTING IN-BAND MULTICAST PORT INFORMATION STORED ALONG WITH THE DATA IN A SINGLE MEMORY WITHOUT MEMORY READ CYCLE OVERHEAD", Serial No. 09/676,171, Filed September 29, 2000.						
S. Babar Raza et al., "LOGIC FOR GENERATING MULTICAST/UNICAST ADDRESS (ES)", Serial No. 09/676,706, Filed September 29, 2000.						
S. Babar Raza et al., "LOGIC FOR INITIALIZING THE DEPTH OF THE QUEUE POINTER MEMORY", Serial No. 09/676,705, Filed September 29, 2000.						
Examiner	Suresh K. Suryawanshi		Date Considered	3/16/04		
Examiner: Initial if references considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.						

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Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY DOCKET NO. 0325.00446	SERIAL NO. 09/775,372
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	APPLICANT: Jiann-Cheng Chen et al.	
	FILING DATE February 1, 2000	GROUP 2151

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub- Class	Filing Date
SKS	5 9 0 5 7 6 6	5/18/1999	Nguyen	375	354	
SKS	5 9 5 1 6 3 5	9/14/1999	Kangar	709	201	
SKS	6 0 3 3 4 4 1	3/7/2000	Herbert	769	400	
SKS	6 0 5 2 1 5 2	4/18/2000	Malcolm, Jr. et al.	348	537	
SKS	6 0 7 5 8 3 3	6/13/2000	Leshay et al.	377	16	
SKS	6 1 3 4 1 5 5	10/17/2000	Wen	365	189.04	

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Class	Sub- Class	Transl.
					Yes No

OTHER DOCUMENTS (Incl. Author, Title, Date, Pertinent Pages, etc.)

S. Babar Raza et al., "METHOD AND LOGIC FOR INITIALIZING THE FORWARD POINTER MEMORY DURING NORMAL OPERATION OF THE DEVICE AS A BACKGROUND PROCESS", Serial No. 09/676,170, Filed September 29, 2000.
S. Babar Raza et al., "METHOD AND/OR ARCHITECTURE FOR IMPLEMENTING QUEUE EXPANSION IN MULTIQUEUE DEVICES", Serial No. 09/714,441, Filed November 16, 2000.
Somnath Paul et al., "FIFO READ INTERFACE PROTOCOL", Serial No. 09/732,685, Filed December 8, 2000.
Somnath Paul et al., "FIFO READ INTERFACE PROTOCOL", Serial No. 09/732,686, Filed December 8, 2000.

Examiner: Suresh K. Suryanarayana Date Considered 2/16/04

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